

When the reactive compensation capacitor is parallel with the nonlinear load, from the load side, there is a risk of parallel resonance between capacitor and inductance in the power system line. Therefore, the harmonic current injected by nonlinear load near the resonant frequency will be greatly amplified.

Abstract: This article reports on exploiting a nonlinear capacitance compensation technique to achieve high linearity for power amplifier (PA) designs in III-V ...

This paper focuses on state-of-charge (SoC) estimation for a lithium-ion battery modeled using a recently developed nonlinear double-capacitor representation that has been shown to be highly accurate. This article focuses on state-of-charge (SoC) estimation for a lithium-ion battery modeled using a recently developed nonlinear double-capacitor representation ...

A nonlinear capacitance compensation technique is presented in this paper to enhance the linearity of a power amplifier (PA) in the GaN process. The method involves placing an MSM varactor device alongside the GaN HEMT device, which works as the ...

Over the past 260 years, capacitors have undergone tremendous development, especially after the time when the vacuum tube was invented. 1 As pulsed power technology has been widely applied in electric armor, electric guns, particle beam accelerators, high power microwave sources, nuclear technique, health care, and other electric power systems, 2,3 ...

Considering non-linearities of the compensation capacitor, non-linear coupled transmission dynamics modelling can be obtained. The frequency splitting phenomenon and transmission characteristics were studied comprehensively, and the following results can be concluded. (1) The MCR-WPT system always has dual natural frequencies which is caused by ...

This paper focuses on hysteresis compensation to improve the accuracy of state-of-charge (SoC) estimation in a lithium-ion battery modeled using a recently developed nonlinear double-capacitor representation. The measurement equation of the model has two nonlinear functions, one of them being significant hysteresis in voltage as a function of the SoC. The hysteresis ...

Among the various compensation topologies, the double-sided Inductor-Capacitor-Capacitor (LCC) compensation topology has the advantage that the resonant frequency and transmitter coil current are ...

Nonlinear loads disturb the flow of electricity from the producer to the consumer. The energy is transferred from the producer to the consumer through mains in the form of fundamental harmonic energy. ... Reactive power compensation capacitor is treated separately. After multiplying these voltages, vectors and the capacitor voltage by ...



In this case, the OpAmp"s response is nonlinear until it is able to resume linear operation without exceeding the slew rate. Such transient behavior is common in switched-capacitor circuits, ...

Optimizing the settling response of an operational amplifier can be a serious design issue in today"s low-power CMOS technologies. Several design challenges emerge when improving the linear and nonlinear responses of an amplifier. In this paper, we developed a settling model for use in design and optimization of two-stage Miller-compensated amplifiers. ...

The non-linear effects on system stability, discontinuous impedance, and. ... Series-capacitor compensation is emerging as a stabilising tool in series compensation and phase shifting FACTS devices. Other applications include series power filters and large motor soft starting schemes. Hybrid use of series and shunt switched capacitors are now ...

Note that compensation capacitor Cc can be treated open at low frequency. Overall gain A v = A v1 \*A v2. Chapter 6 Figure 03 Example 6.1 (page 244) ... called slew rate. In this case, the OpAmp's response is nonlinear until it is able to resume linear operation without exceeding the slew rate. Such transient behavior is common in switched ...

@article{Zhang2017AnIM, title={An improved method of SAPF for harmonic compensation and resonance damping with current detection of power capacitors and linear/nonlinear loads}, author={Yuxiao Zhang and Ke Dai and Xinwen Chen and Yong Kang and Ziwei Dai}, journal={2017 IEEE Applied Power Electronics Conference and Exposition (APEC)}, ...

Objective of compensation is to achieve stable operation when negative feedback is applied around the op amp. Types of Compensation 1. Miller - Use of a capacitor feeding back around a high-gain, inverting stage. o Miller capacitor only o Miller capacitor with an unity-gain buffer to block the forward path through the compensation capacitor.

Request PDF | On May 25, 2021, Hamidreza Movahedi and others published Hysteresis Compensation in State-of-Charge Estimation with a Nonlinear Double-Capacitor Li-Ion Battery Model | Find, read and ...

A nonlinear capacitance-compensation technique is developed to help improve the linearity of CMOS class-AB power amplifiers. The method involves placing a PMOS device alongside the ...

The gyro employs the time-multiplexed to reduce gain and phase errors [54] and directly eliminate the influence of parasitic capacitance on the SFN [25]. The geometry of HRG and its closed-loop control system schematic are depicted in Fig. 1. The inner surfaces of the resonator and electrode base are coated with a thin layer of platinum, where the electrode base is ...

This article focuses on state-of-charge (SoC) estimation for a lithium-ion battery modeled using a recently developed nonlinear double-capacitor representation that has been shown to be highly accurate. The



measurement equation of the model has two nonlinear functions, one of them being significant hysteresis in voltage as a function of the SoC. The hysteresis term is modeled ...

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In Fig. 4.12, the Value of compensation capacitor versus triac fire angle alpha is displayed. ... and unbalance can be amended with a detached system of capacitors or inductors. Nonlinear loads, for example, rectifiers, contort the current drawn from the framework. In such cases, dynamic or aloof power variable remedy might be utilized to check ...

The nonlinear capacitor models are based on the static and transient measurements. A. SPICE Model for Nonlinear Capacitor. The Analog Behavioral Model is used to model the nonlinear capacitor, as shown in Fig. 14. The capacitor is modeled by a controlled current source, GVALUE in PSpice, whose current is defined by equation (2).

An improved method of SAPF for harmonic compensation and resonance damping with current detection of power capacitors and linear/nonlinear loads ... two different load current detection points are introduced. With traditional method, hybrid compensation system is stable when capacitor current is not included in the detected load current. ...

High-resolution electronic interface circuits for transducers with nonlinear capacitive impedance need an operational amplifier, which is stable for a wide range of load capacitance. ... The prototype amplifier consumes 7.6 mW from a single 1.0-V supply. For a given compensation capacitor size and a chip area, the prototype design demonstrates ...

A new nonlinear compensation technique to capacitor pressure sensor based on least square support vector regression (LSSVR) is proposed, which effectively compensates any variation of the CPS"s output occurring due to change in environmental temperature. A new nonlinear compensation technique to capacitor pressure sensor (CPS) based on least ...

Here"s an intuitive, non-mathematical look at nonlinear slope compensation in current-mode buck regulators and the benefits it provides in securing stable operation across the regulator"s operating voltage range

The DFR problem is intrinsically complex and nonlinear; combination with capacitor allocation the problem becomes more complex than before, hence a precise optimization method is required to solve ...

Besides, it avoids PF penalties. Linear loads with low PF can be corrected with passive elements such as capacitors or/and inductors. Non-linear loads, such as rectifiers, distort the current drawn from the system. ... The starting time for connecting the compensation capacitor bank(s) is 0.1 s (i.e. at the sample number of 500) from the ...



Among the various compensation topologies, the double-sided Inductor-Capacitor-Capacitor (LCC) compensation topology has the advantage that the resonant frequency and transmitter coil current are independent of the coupling coefficient and load [10,11,12,13,14]. However, this compensation topology has a constant current output ...

High-resolution electronic interface circuits for transducers with nonlinear capacitive impedance need an operational amplifier, which is stable for a wide range of load capacitance. Such operational amplifier in a conventional design requires a large area for compensation capacitors, increasing cos ...

As in the DAC case, there is a compensation capacitor with 3C u at the SUM node to let the sum of capacitances along the input load be equal to 32C u and represent the result of multi-bit MAC. To generate appropriate reference voltages for comparing with the SUM node to convert the analog MAC results into output bits, there is also a compensation ...

Constant-current charging is the optimal solution for charging linear (fixed) capacitors. In this letter, we extend this principle to nonlinear capacitors using a variational method. We address the case where the capacitance depends only on the applied voltage. We show that a nonlinear capacitor stores energy electrostatically and by another mean, ...

Abstract: The proposed switched capacitor converter for energy harvesting applications can have a wide input range of 0.25V to 2V. The input nonlinear adaptive voltage position (INAVP) control and feedforward compensation can decrease DV out and the settling time in line transients. The mismatch calibration circuit can reduce the influence caused by the mismatch in different phase.

In this paper, we propose a nonlinear capacitor cancellation technique to cancel the nonlinear input capacitance of the amplifier, thus improving the large-signal Class-AB linearity. A ...

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